	Notice of References Cited	Application/Control No. 09/590,796	Applicant(s)/Patent Under Reexamination VREUGDENHIL ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,532,569	03-2003	Christen et al.	716/2
	B	US-6,236,956	05-2001	Mantooth et al.	703/14
	C	US-5,548,539	08-1996	Vlach et al.	703/6
	D	US-4,985,860	01-1991	Vlach, Martin	703/17
	E	US-4,868,770	09-1989	Smith et al.	703/14
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	IEEE Standard 1076.1-1999. March 18, 1999.
	V	Christen, E. et al. "VHDL-AMS - A Hardware Description Language for Analog and Mixed-Signal Applications". IEEE Transactions on Circuits and Systems II: Analog and Digital Signal Processing. Vol. 46, Issue 10, Oct. 1999. pp.1263-1272.
	W	Christen, E. et al. "VHDL 1076.1 - Analog and Mixed-Signal Extensions to VHDL". Proceedings EURO-DAC '96. Sept. 20, 1996. pp.556-561.
	X	Damon, D. et al. "Introduction to VHDL-AMS. 1. Structural and Discrete Time Concepts." Proc. 1996 IEEE Int'l Symposium on Computer-Aided Control System Design. Sept. 18, 1996. pp.264-269

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

		Application/Control No.	Applicant(s)/Patent Under Reexamination
Notice of References Cited		09/590,796	VREUGDENHIL ET AL.

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Christen, E. et al. "Introduction to VHDL-AMS. 2. Continuous and Mixed Continuous/Discrete Concepts." Proc. 1996 IEEE Int'l Symposium on Computer-Aided Control System Design. Sept. 18, 1996. pp.270-275
	V	Shi, R. "VHDL-A: Analog Extension to VHDL". Proc. 7th Annual IEEE Int'l ASIC Conf. and Exhibit, 1994. Sept. 23, 1994. pp.160-165.
	W	Saleh, et al. "Analog Hardware Description Languages". Proc. of IEEE 1994 Custom Integrated Circuits Conf. May 4, 1994. pp.349-356.
	X	Mantooth, H.A. et al. "Beyond Spice with Saber and MAST". 1992 IEEE Int'l Symposium on Circuits and Systems (ISCAS '92). May 13, 1992. vol.1, pp.77-80.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited

Application/Control No. 09/590,796	Applicant(s)/Patent Under Reexamination VREUGDENHIL ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Vlach, M. "Modeling and Simulation with Saber". Proc., 3rd Annual IEEE ASIC Seminar and Exhibit, 1990. Sept. 21, 1990. pp.T/11.1-T/1111.
	V	Vlach, J. "Computer Oriented Formulation of Equations and Analysis of Switch-Capacitor Networks". IEEE Transactions on Circuits and Systems. Vol.31, Issue 9, Sept. 1994. pp.753-765.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.